

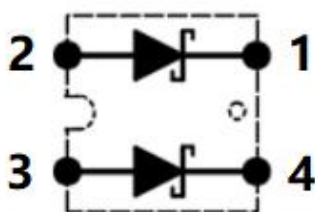
S5D100170S2 1700V SiC POWER SCHOTTKY RECTIFIER



Description

S5D100170S2 is a SiC Schottky rectifier packaged in SOT-227 case. The device is high voltage Schottky rectifier that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S5D100170S2 is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Features

- 175°C T_J operation
- Ultra-low switching loss
- Switching speeds independent of operating temperature
- Low total conduction losses
- High forward surge current capability
- High package isolation voltage
- Terminals finish: 100% Pure Tin
- "-A" is an AEC-Q101 qualified device
- Pb - Free Device
- All SMC parts are traceable to the wafer lot
- Additional electrical and life testing can be performed upon request

Applications

- Alternative energy inverters
- Power Factor Correction (PFC)
- Free-Wheeling diodes
- Switching supply output rectification
- Reverse polarity protection

Maximum Ratings(per leg)

Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage	V_{RRM}	-	1700	V
Working Peak Reverse Voltage	V_{RWM}			
DC Blocking Voltage(per leg)	V_R			
Average Rectified Forward Current(per leg)	$I_{F(AV)1}$	$T_C = 25^\circ\text{C}$	78	A
	$I_{F(AV)2}$	$T_C = 108^\circ\text{C}$	50	A
Peak One Cycle Non-Repetitive Surge Current(per leg)	I_{FSM1}	10ms, Half Sine pulse, $T_C = 25^\circ\text{C}$	560	A
	I_{FSM2}	10ms, Half Sine pulse, $T_C = 110^\circ\text{C}$	420	A
Repetitive Peak Forward Surge Current(per leg)	I_{FRM1}	10ms, Half Sine pulse, $T_C = 25^\circ\text{C}$	336	A
	I_{FRM2}	10ms, Half Sine pulse, $T_C = 110^\circ\text{C}$	244	A
Power Dissipation(per leg)	P_{tot1}	$T_C = 25^\circ\text{C}$	517	W
	P_{tot2}	$T_C = 110^\circ\text{C}$	224	W

Electrical Characteristics(per leg)

Characteristics	Symbol	Condition	Typ.	Max.	Units
Forward Voltage Drop*	V_{F1}	@ 50A, Pulse, $T_J = 25\text{ }^{\circ}\text{C}$	1.55	1.8	V
	V_{F2}	@ 50A, Pulse, $T_J = 175\text{ }^{\circ}\text{C}$	2.5	3.0	V
Reverse Current at DC condition*	I_{R1}	@ V_R = rated V_R , $T_J = 25\text{ }^{\circ}\text{C}$	2	20	μA
Reverse Current *	I_{R2}	@ V_R = rated V_R , $T_J = 175\text{ }^{\circ}\text{C}$	40	400	μA
Junction Capacitance	C_T	$V_R=0\text{V}$, $f=1\text{MHz}$, $T_J=25\text{ }^{\circ}\text{C}$	4500	-	pF
Reverse Recovery Charge	Q_c	$V_R = 1700\text{ V}$, $T_J=25\text{ }^{\circ}\text{C}$	515	-	nC
Capacitance Stored Energy	E_c	$V_R = 1700\text{ V}$, $T_J=25\text{ }^{\circ}\text{C}$	574	-	μJ

* Pulse width < 300 μs , duty cycle < 2%

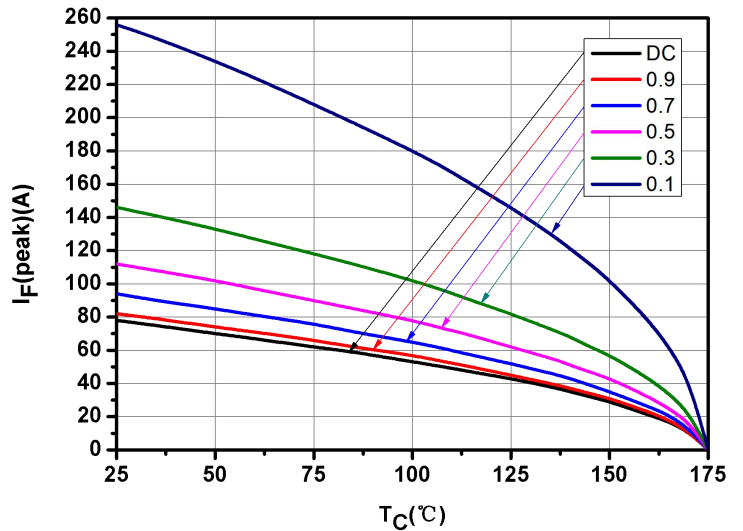
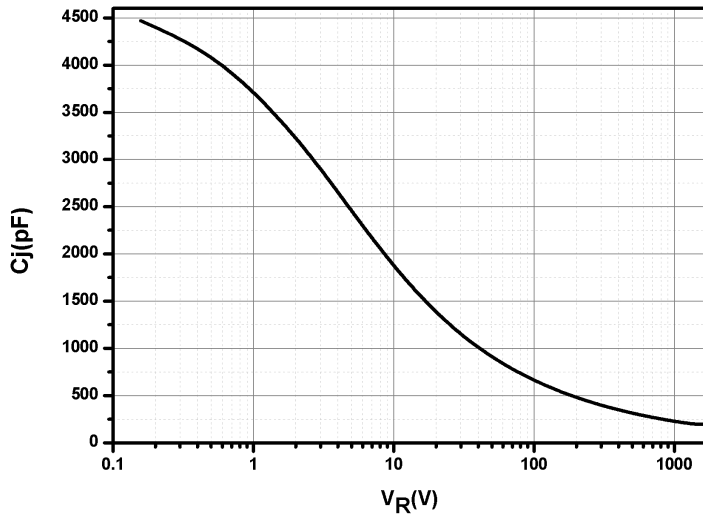
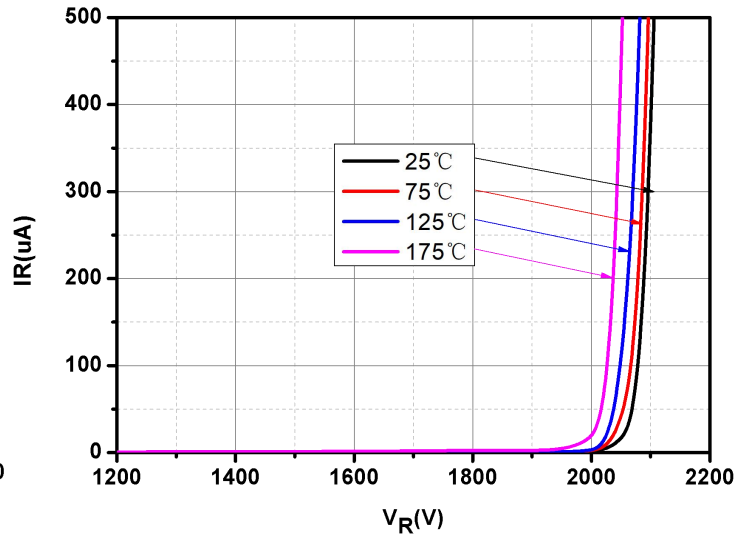
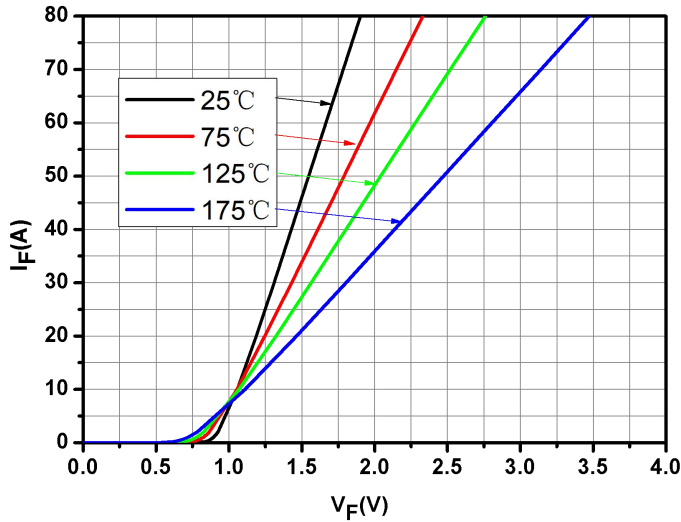
Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T_J	-	-55 to +175	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-	-55 to +175	$^{\circ}\text{C}$
Typical Thermal Resistance Junction to Case(per leg)	$R_{\theta JC}$	DC operation, $T_J=25\text{ }^{\circ}\text{C}$	0.29	$^{\circ}\text{C/W}$

Ordering Information

Device	Package	Shipping
S5D100170S2	SOT-227	36pcs / BULK

Ratings and Characteristics Curves(per leg)



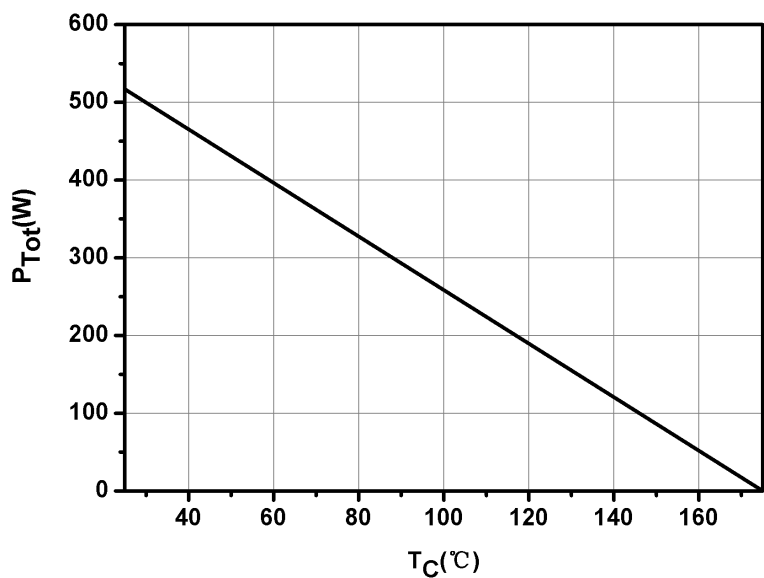


Fig.5-Power Derating

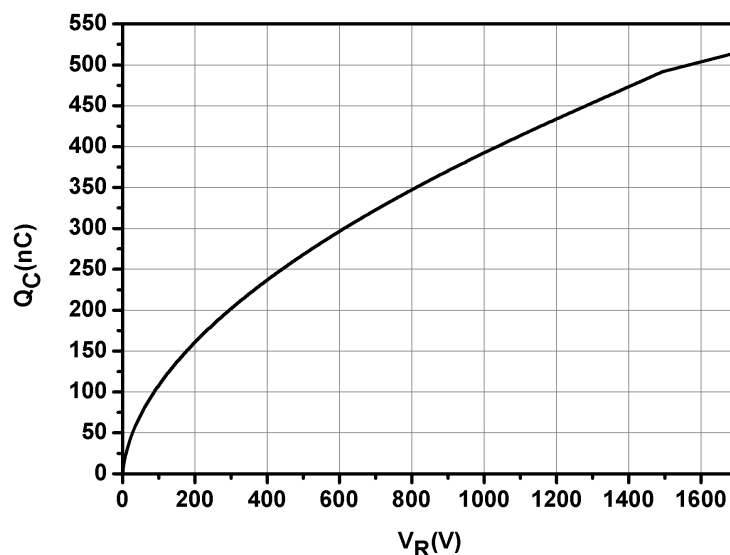


Fig.6-Total Capacitance Charge vs. Reverse Voltage

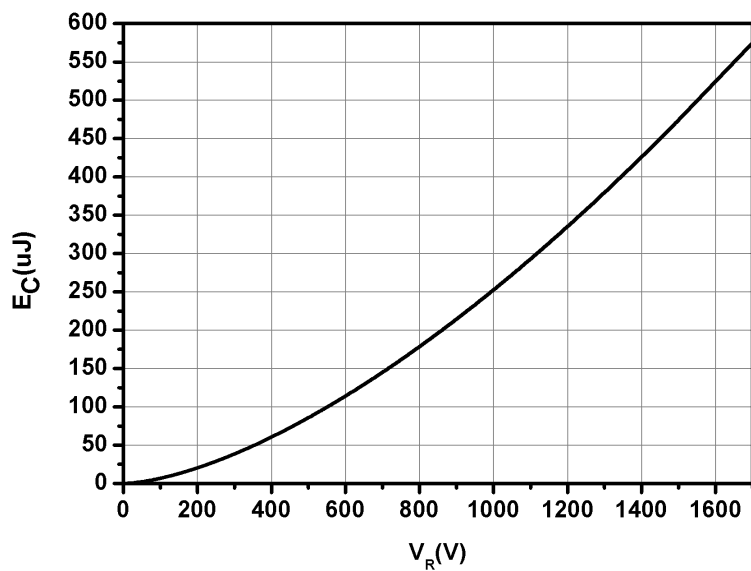
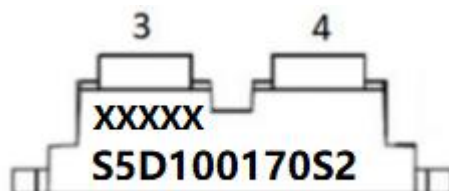


Fig.7-Capacitance Stored Energy

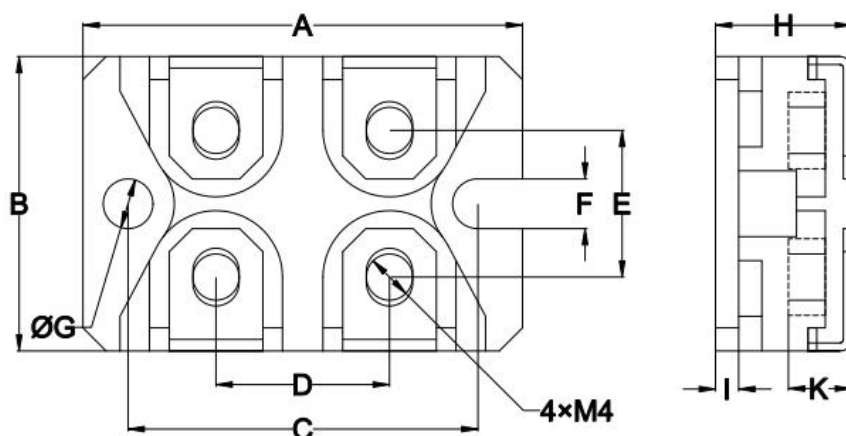
Marking Diagram



Where XXXXX is YYWWL

S5D = Device Type
S2 = Package type
100 = Forward Current (100A)
170 = Reverse Voltage (1700V)
SSG = SSG
YY = Year
WW = Week
L = Lot Number

Mechanical Dimensions SOT-227



SYMBOL	Dimensions in millimeters	
	Min.	Max.
A	37.8	38.2
B	24.8	25.2
C	29.9	30.5
D	14.5	15.5
E	12.2	13.2
F	4.1	4.31
G	φ 4.1	φ 4.31
H	11	12.5
I	1.9	2.1
K	4.3	6.5

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